

CLAIMS:

1. A process of selecting different 1149.1 TAP domain arrangements within an integrated circuit comprising the steps of:

performing an 1149.1 instruction shift operation through a first 1149.1 TAP domain arrangement,

performing an 1149.1 instruction update operation at the end of said 1149.1 instruction shift operation, and;

in response to said 1149.1 instruction update operation, selecting a second 1149.1 TAP domain arrangement which differs from the first 1149.1 TAP domain arrangement.

2. An 1149.1 TAP linking module within an integrated circuit comprising:

a TDI input and a TDO output;

a TAP controller;

an instruction register having a serial input connected to said TDI input, a serial output, and control inputs connected to said TAP controller;

a multiplexer having a first input connected to said TDI input, a second input connected to said instruction register serial output, control input connected to said TAP controller, and an output connected to said TDO output.